

# Nonlinear control of a DC MicroGrid for the integration of distributed generation based on different time scales

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**Abstract**—This paper constitutes a contribution for DC MicroGrid control and introduces a rigorous dynamics' analysis, taking into account intermittency effects. The stabilization of the system is based on storage devices: batteries for energy balance and long term response of power flow, while supercapacitors deal with power balance and fast response.

We propose an hierarchical control scheme, based on nonlinear control theory, in particular Lyapunov, backstepping and input/output feedback linearization. The resulting algorithms are simple, comparable to standard nested PIs, and do not need important computer resources, such to be easily implemented in small micro-controllers. In addition to allowing an explicit stability analysis, they present the advantage of preserving performance for the whole operation region, and to be easy to tune.

The proposed DC MicroGrid and its control are then verified either by computer simulations and by experiments. The results show the good performance of the system under variations on production and on consumption.

**Index Terms**—Power generation control, Nonlinear control systems, Power system control, Energy storage, Microgrids, DC Grids, Stability, Lyapunov methods.

## I. INTRODUCTION

**D**UE to their intermittent nature, the large penetration of Distributed Generation (DG) can cause disturbances in the utility grid. To guarantee high quality electricity in this framework, it is interesting to produce and consume this distributed power close by, assuring locally the power balance with storage units, and then MicroGrids can be a good solution to attain this goal.

The MicroGrid requires a suitable control strategy to provide a continuous and sufficient energy to load demand, balancing all power fluxes [10], [9], and it can be connected to the main network or not. In the present work a MicroGrid is used to integrate DG to a time varying load, using storage system devices working in different time scales (battery and supercapacitor). This basic scheme can be easily generalized as each

element may represent the aggregation of similar elements, and in particular the load can also aggregate the interconnection to the main grid [8]. The concept of Microgrid with storage devices can be the answer of interconnection constraints, and an efficient way to bring such power to the main grid [1], as well as providing ancillary services as voltage, frequency and inertia support. The power flow management and a rather complex control strategy become mandatory for such systems, and in the present case is constituted by an hierarchical control scheme. Although several control schemes are already applied to MicroGrids, as explained in the following they have hard limits when dealing with strong varying operating conditions; they usually not allow a formal stability proof, that leads to formal estimations of operation regions; they are very hard to tune, usually relying on exhaustive try and error by simulations to find control gains. The proposed control methods can deal with such drawbacks, and are much easier to tune than those found in the literature. Finally, they are expected to be easier to replicate in a distributed and interconnected way. In future works, it will be studied the interconnection of several similar MicroGrids in a systematic way.

Since several sources, storage systems and loads are natively DC, in order to reduce power losses and to guarantee a better power quality we will use a DC MicroGrid. The challenge is to design a control structure (either for the whole grid than for each power converter) in such way that the interconnection of the DG to the network does not disturb the main grid, and to assure stability of the whole system under variation of production and consumption. The usual approach for controlling MicroGrids is linear control due its simplicity. In addition, it is widely known in both industry and academia, either in frequency domain via transfer functions as in space state, where nested PIDs, and sometimes robust techniques as LQR are the most used strategies. For example, the minor loop gain is a linear controller presented in [16] that relates source and load impedance's to determine stability in the grid. This controller has the advantage of maintaining system dynamics even when connecting more devices like filters. This impedance-based approach is also well fit when using state space modeling [2], [20].

Nevertheless, linear control in general is based on a linearized model, designed for only one operating point. This may represent an important inconvenient for the integration

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of varying sources like renewables, applied to also varying loads [14], [7], [17]. In addition to overcome limitations of linear theory, the real systems are split in several sub-systems with dedicated controllers for each. The final structure includes nested controllers, where the effects of one controller and its tuning on the others is never formally established. The system may then become very complex, and their tuning a real challenge. For all these reasons, nonlinear theory may provide a much broader approach that can cover all aspects, and provide rigorous analysis of the overall system for a well defined operation region. In addition, the final controllers are not much more complex than linear ones, and end up much simpler to tune, because their tuning comes from the Lyapunov function and the desired dynamics.

Here the System of systems approach is used: the control is designed globally and accomplished locally (see [3] and [15]), also used in [22]. In the present case, the control structure of the DC MicroGrid is designed with two levels, the low level controller based on nonlinear control theory (Lyapunov, backstepping and input/output feedback linearization) assures exponential stability of each element to its own reference. On the top of that, high level controllers give references based on different objectives like power production optimization (MPPT); maximizing storage capacity; and in particular to accomplish power flow in all branches of the DC grid [6], [11]. The system's stability is formally analyzed by a constructive Lyapunov function, and assures exponential stability of the whole system under mild conditions, that in their turn define the operation conditions of the grid. The obtained system will then guarantee good dynamic performance and flexibility.

The theoretical development is then verified first by computer simulations, and then in an experimental setup. All tests show the good performance of the proposed control scheme for random variations on load and on generation.

## II. SYSTEM CONFIGURATION

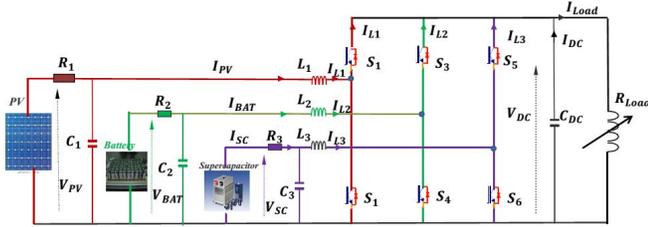


Figure 1: Three-Input DC – DC Boost Converter Circuit

The structure of the DC MicroGrid is shown in figure (1). The PV array, battery and supercapacitor are connected to the DC MicroGrid by a Three-Input DC – DC Boost Converter. One unidirectional leg for PV input as source, two bidirectional legs for the storage elements, and the load is connected to the DC Microgrid.

### A. Model of Three-Input DC/DC Boost Converter

This converter consists of a Three-Input DC/DC Boost Converter with six switches connected in parallel per phase. These

switches are independently controlled with three different duty cycles. In the following it will be used these duty cycles to control the power flow among PV, storage and load.

The mathematical model is given based on power electronics averaging technique (see [18], [4] and [21]) controlled using Pulse Width Modulation (PWM). The state variables in this model are the capacitor voltages  $V_{C1}, V_{C2}, V_{C3}$ , inductor currents  $i_{L1}, i_{L2}, i_{L3}$  and DC MicroGrid voltage  $V_{DC}$ .

$$\begin{aligned}
 \frac{dV_{C1}}{dt} &= -\frac{1}{R_1 C_1} V_{C1} - \frac{1}{C_1} i_{L1} + \frac{1}{R_1 C_1} V_{PV} \\
 \frac{di_{L1}}{dt} &= \frac{1}{L_1} V_{C1} - \frac{[(R_{01} - R_{02})u_1 + R_{02}]i_{L1}}{L_1} - \frac{(1 - u_1)}{L_1} V_{DC} \\
 \frac{dV_{C2}}{dt} &= -\frac{1}{R_2 C_2} V_{C2} - \frac{1}{C_2} i_{L2} + \frac{1}{R_2 C_2} V_{BAT} \\
 \frac{di_{L2}}{dt} &= \frac{1}{L_2} V_{C2} - \frac{[(R_{03} - R_{04})u_2 + R_{04}]i_{L2}}{L_2} - \frac{(1 - u_2)}{L_2} V_{DC} \\
 \frac{dV_{C3}}{dt} &= -\frac{1}{R_3 C_3} V_{C3} - \frac{1}{C_3} i_{L3} + \frac{1}{R_3 C_3} V_{SC} \\
 \frac{di_{L3}}{dt} &= \frac{1}{L_3} V_{C3} - \frac{[(R_{05} - R_{06})u_3 + R_{06}]i_{L3}}{L_3} - \frac{(1 - u_3)}{L_3} V_{DC} \\
 \frac{dV_{DC}}{dt} &= \frac{1}{C_{DC}} [(1 - u_1)i_{L1} + i_{Storage} - i_{Load}] \quad (1)
 \end{aligned}$$

where  $R_1, R_2, R_3, L_1, L_2, L_3, C_1, C_2, C_3$  and  $C_{DC}$  are known positive values of resistances, capacitors and inductors. For modeling the switches it was included the small resistances ( $R_{01}, R_{02}, R_{03}, R_{04}, R_{05}$  and  $R_{06}$ ) when they are driving, so the conduction losses are taken into account.  $V_{PV}, V_{BAT}, V_{SC}$  and  $V_{DC}$  are the photovoltaic panel, battery, supercapacitor and DC MicroGrid Voltages respectively. The output measurement state is composed by  $V_{C1}, V_{C2}, V_{C3}, i_{L1}, i_{L2}, i_{L3}$  and  $V_{DC}$ .  $u_1, u_2$  and  $u_3$  will be the control inputs which define the duty cycles of the converter.

## III. GRID CONTROL STRATEGY

The objective of this work is to keep DC MicroGrid voltage in its nominal value and to assure stability of the whole system. To attain this goal, high level controllers provide references for the local ones. This higher level task is split in different objectives. The high level controller for the PV follows a MPPT algorithm. The high level controller for the storage system is given by a power flow management in DC MicroGrid. In this part, the DC MicroGrid voltage  $V_{DC}$  is compared to its reference value  $V_{DC}^*$ , then it is computed the total current required to be provided/absorbed by the storage such as to fulfill the power flow and steer the DC grid voltage to its reference. We use a low-pass filter to split this current into two references. The average (low frequency) component of the signal is the reference for the battery current control. The dynamic (higher frequency) component becomes the reference for the supercapacitor current control.

The objective of this control strategy is to use each storage element in a different time scale; the role of the battery is to support slow transitory flow, while the supercapacitor supports fast transitory flows.

## A. Control Structure

Figure (2) shows the block diagram representation of the Microgrid control strategy, the objective of this Algorithm is to stabilise DC Microgrid voltage and to feed correctly a load of desired power while reduce battery charge/discharge cycles.

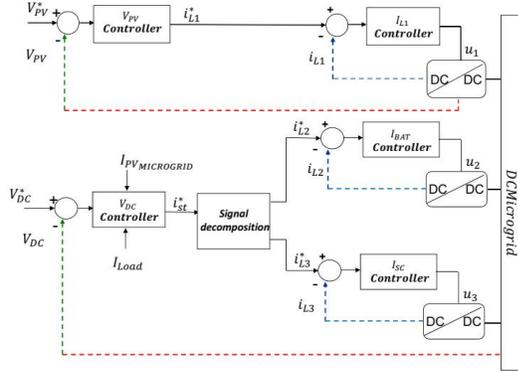


Figure 2: Structure Control

## B. High level controller

1) *High level controller for the Power Source*: In order to maximize the solar panels producing capability, it is necessary to control its operating point to draw the maximum power from it. The algorithm of Maximum Power Point Tracking (MPPT) used in this work is an Incremental Conductance (IncCond) which delivers a reference voltage  $V_{C1}^*$  to be tracked by the lower level controller.

2) *High level controller for the storage system*: The proposed Hybrid storage consists of a Li-Ion Battery and a supercapacitor. The target of the control is to charge/discharge the storage component and to provide grid voltage stability during transients. The power needed to fulfill the power flow is represented by a current flowing from/to the storage to/from the DC-link capacitance.

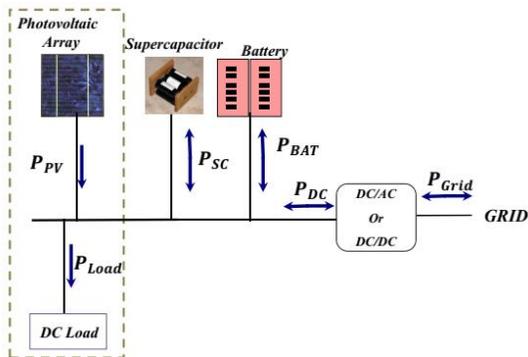


Figure 3: Power Flow

The power flow on the components in the proposed DC MicroGrid is shown in figure (3) (see [12]). The sum of the output power of the photovoltaic array, and the storage is defined as follows:

$$P_{DC} = P_{PV} + P_{Load} + P_{ST} \quad (2)$$

$$P_{ST} = P_{BAT} + P_{SC} \quad (3)$$

With  $P_{PV}$ : PV power,  $P_{ST}$ : Storage system power,  $P_{BAT}$ : Battery power,  $P_{SC}$ : Supercapacitor power,  $P_{Load}$ : Load power,  $P_{DC}$ : DC grid power. from equation (2)

$$C_{DC} V_{DC} \frac{dV_{DC}}{dt} = V_{DC} [i_{PV} + i_{Storage} - i_{Load}] \quad (4)$$

which gives us the last equation of the system (1)

The reference value for  $V_{DC}$  is the constant  $V_{DC}^*$ , a backstepping procedure is then adopted such as to design an  $i_{ST} = i_{L2}^* + i_{L3}^*$  reference for  $i_{L2}$  and  $i_{L3}$  that will provide new desired dynamics for the DC bus voltage.

In our study, the system is sized such that the battery and supercapacitor stay in  $40\% < SOC < 80\%$ , so we work in the linear phase of the battery and supercapacitor.

## IV. SYSTEM NONLINEAR CONTROL

### A. Storage System Control Law

1) *voltage Control (High Level Controller)*: The outer voltage loop is used to stabilise  $V_{DC}$  then  $V_{DC}^*$  is determined from the last equation of the system (1) by the following method:

Lets first define the output tracking error:

$$e_{V_{DC}} = (V_{DC} - V_{DC}^*)$$

From the system (1)

$$\frac{dV_{DC}}{dt} = \frac{1}{C_{DC}} [(1 - u_1)i_{L1} + i_{Storage} - i_{Load}] \quad (5)$$

and the desired dynamics of  $V_{DC}$  as:

$$\begin{aligned} \dot{\alpha}_7 &= K_7^\alpha e_{V_{DC}} \\ \dot{e}_{V_{DC}} &= -K_7 (V_{DC} - V_{DC}^*) - \bar{K}_7 \alpha_7 \end{aligned} \quad (6)$$

Where  $\alpha_7$  is now defined as an extended state (integral term).

From (5) and (6) we can design:

$$\begin{aligned} i_{ST}^* &= -K_7 C_{DC} (V_{DC} - V_{DC}^*) - \bar{K}_7 C_{DC} \alpha_7 \\ &\quad - (1 - u_1)i_{L1} + \frac{V_{DC}}{R_{Load}} \end{aligned} \quad (7)$$

with  $K_7$ ,  $\bar{K}_7$  and  $K_7^\alpha$ , positive gains that are used to impose desired dynamics to the closed loop system. Indeed we can write:

$$\dot{\tilde{V}}_{DC} = \begin{bmatrix} \dot{\alpha}_7 \\ \dot{e}_{V_{DC}} \end{bmatrix} = \begin{bmatrix} 0 & K_7^\alpha \\ -\bar{K}_7 & -K_7 \end{bmatrix} \tilde{V}_{DC}$$

which closed loop (linear) dynamics is here defined with the eigenvalues:

$$\lambda_{7,\alpha_7} = -\frac{1}{2} (K_7 \pm \sqrt{K_7^2 - 4\bar{K}_7 K_7^\alpha})$$

As a corollary, there exists  $P_7 = P_7^T > 0$  such that  $P_7 A_7 + A_7^T P_7 = -I_2$  where  $I_2$  is the identity matrix in  $\mathbb{R}^{2 \times 2}$ .

We introduce now the Lyapunov function:

$$V_7 = [\alpha_7 e_{V_{DC}}]^T P_7 [\alpha_7 e_{V_{DC}}] \quad (8)$$

It's derivative is then:

$$\dot{V}_7 = -e_{V_{DC}}^2 - \alpha_7^2 \quad (9)$$

Now using the property (see [13]) :

$$\lambda_{\min}(P_7) \|x\|^2 \leq x^T P_7 x \leq \lambda_{\max}(P_7) \|x\|^2 \quad (10)$$

we can then write that ( $m_7$  is a positive constant):

$$\dot{V}_7 \leq -\frac{1}{\lambda_{\max}(P_7)} V_7 \triangleq -m_7 V_7 \quad (11)$$

that implies that:

$$\dot{V}_7(t) = e^{-m_7 t} V_7(0) \quad (12)$$

and then we can conclude that:

$$\lim_{t \rightarrow \infty} \left\| \begin{bmatrix} e_{V_{DC}}(t) \\ \alpha_7(t) \end{bmatrix} \right\| = 0$$

and that the closed-loop (linear) dynamics of  $V_{DC}$  are exponentially stable. **Control Parameters:**

The system transfer function can be written as:(see [5] and [19])

$$H_7(s) = \frac{1}{s^2 + 2\zeta_7 \omega_7 s + \omega_7^2} = \frac{1}{s^2 + K_7 s + \bar{K}_7 K_7^\alpha} \quad (13)$$

we can set  $K_7^\alpha = 1$

One can then use a simple identification to know the value of the  $K_7$  and  $\bar{K}_7$  parameters.

$$\zeta_7 = \frac{K_7}{2\sqrt{\bar{K}_7 K_7^\alpha}}$$

$$\omega_7 = \sqrt{\bar{K}_7 K_7^\alpha}$$

We wanted to dissociate the current controller and the voltage controller so that a simple and efficient control system could be set. In order to achieve it, we decided to separate the time constants of the controllers; this is why we decided to have a ratio of 100 between  $\tau_{icurrent}$  and  $\tau_{ivoltage}$ (that is true experimentally: the current variations are really quicker than the voltage variations).

and  $\omega_7 = \frac{2\pi}{T} = \frac{2\pi}{0.1} = 62.831 \text{rad/s}$  I also chose  $\zeta_7 = 0.7$  since It was chosen  $\zeta_7 = 0.7$  since it provides a quick response without much overshoot. Now, the reference (7) in its turn is split into a Fast and Slow terms by a low pass filter shown in Fig. 2.

such that:

<sup>1</sup>Such matrix always exist because of the stable poles assigned by the tuning gains.

$$i_{ST}^* = i_{L2}^* + i_{L3}^*$$

and then  $i_{L2}^*$  provided to the battery acts to maintain the balance of power and  $i_{L3}^*$  provided to the supercapacitor acts to dampen fast transitory and oscillations.

2) *Current Control for the Battery*:: we need to design a control law  $u_2$  such as to steer  $i_{L2}$  towards its reference  $i_{L2}^*$ . First we define its desired closed loop dynamic:

From system (1):

$$\dot{i}_{L2} = \frac{1}{L_2} V_{C2} - \frac{[(R_{03} - R_{04})u_2 + R_{04}]i_{L2}}{L_2} - \frac{(1 - u_2)}{L_2} V_{DC} \quad (14)$$

the tracking error is:

$$e_{i_{L2}} = i_{L2} - i_{L2}^* \quad (15)$$

Following the backstepping technique, the desired dynamics for  $i_{L2}$  is introduced as:

$$\begin{aligned} \dot{\alpha}_4 &= K_4^\alpha e_{i_{L2}} \\ \dot{e}_{i_{L2}} &= -K_4 e_{i_{L2}} - \bar{K}_4 \alpha_4 \end{aligned} \quad (16)$$

where  $\alpha_4$  is defined as an extended state, and positive tuning gain parameters  $K_4, \bar{K}_4$  and  $K_4^\alpha$  that assign the desired dynamics.

From equations (14) and (16) and as in the previous step we obtain the following control law that attain the desired closed loop dynamics:

$$u_2 = \frac{1}{(V_{DC} + (R_{04} - R_{03})i_{L2})} [V_{DC} - V_{C2} + R_{04}i_{L2} - L_2(K_4 e_{i_{L2}} + \bar{K}_4 \alpha_4 - \dot{i}_{L2}^*)] \quad (17)$$

With condition  $V_{DC} + (R_{04} - R_{03})i_{L2} \neq 0$  (which is always true by physical reasons). The same procedure is repeated to design control Parameters for current control loop of the battery Then:  $K_4 = 8796.2, \bar{K}_4 = 6283^2$  and  $K_4^\alpha = 1$

3) *Control for Supercapacitor*: The control input  $u_3$  is dedicated to control the dynamics  $i_{L3}$  to ensure a desired charge/discharge of the supercapacitor.

From system (1):

$$\frac{di_{L3}}{dt} = \frac{1}{L_3} V_{C3} - \frac{[(R_{05} - R_{06})u_3 + R_{06}]i_{L3}}{L_3} - \frac{(1 - u_3)}{L_3} V_{DC} \quad (18)$$

The tracking error is:

$$e_{i_{L3}} = i_{L3} - i_{L3}^* \quad (19)$$

Following the backstepping technique, the desired dynamics for  $i_{L3}$  is introduced as:

$$\begin{aligned} \dot{\alpha}_6 &= K_6^\alpha e_{i_{L3}} \\ \dot{e}_{i_{L3}} &= -K_6 e_{i_{L3}} - \bar{K}_6 \alpha_6 \end{aligned} \quad (20)$$

From equations (18) and (20) and with the positive tuning gain parameters  $K_6, K_6^\alpha$  and  $\bar{K}_6$ , we obtain the control law:

$$u_3 = \frac{1}{(V_{DC} + (R_{06} - R_{05})i_{L3} + R_{06}i_{L3} - L_3(K_6 e_{i_{L3}} + \bar{K}_6 \alpha_6 - \dot{i}_{L3}^*))} [V_{DC} - V_{C3}] \quad (21)$$

with condition  $V_{DC} + (R_{06} - R_{05})i_{L3} \neq 0$  (again always true by physical reasons). The same procedure is repeated to design control Parameters for current control loop of the battery Then:  $K_6 = 87963.4, \bar{K}_6 = 62831^2$  and  $K_6^\alpha = 1$ .

We introduce now the Lyapunov function where  $P_{4,6}$  fulfill the Lyapunov's equation:

$$V_{4,6} = \frac{1}{2}([\alpha_4 e_{i_{L2}}]^T P_4[\alpha_4 e_{i_{L2}}]) + \frac{1}{2}([\alpha_6 e_{i_{L3}}]^T P_6[\alpha_6 e_{i_{L3}}]) \quad (22)$$

It's derivative is then:

$$\dot{V}_{4,6} = -e_{i_{L2}}^2 - \alpha_4^2 - e_{i_{L3}}^2 - \alpha_6^2 \quad (23)$$

which implies that the closed loop system for the battery and supercapacitor is exponentially stable.

### B. Control law for PV

Let us first focus on the control  $u_1$  dedicated to stabilize dynamics  $V_{C1}$  and  $i_{L1}$ : it is developed by using a backstepping technique such as to design an  $i_{L1}^*$  reference value to  $i_{L1}$  that is used to steer voltage  $V_{C1}$  to its equilibrium point following a desired dynamic behavior to be imposed. From the system 1

$$\begin{aligned} \frac{dV_{C1}}{dt} &= -\frac{1}{R_1 C_1} V_{C1} - \frac{1}{C_1} i_{L1} + \frac{1}{R_1 C_1} V_{PV} \\ \frac{di_{L1}}{dt} &= \frac{1}{L_1} V_{C1} - \frac{[(R_{01} - R_{02})u_1 + R_{02}]i_{L1}}{L_1} - \frac{(1 - u_1)}{L_1} V_{DC} \end{aligned} \quad (24)$$

Lets first define the output tracking errors:

$$\begin{aligned} e_{V_{C1}} &= (V_{C1} - V_{C1}^*) \\ e_{i_{L1}} &= (i_{L1} - i_{L1}^*) \end{aligned}$$

The desired dynamics for  $V_{C1}$  and  $i_{L1}$  are introduced as:

$$\dot{e}_{V_{C1}} = -K_1 e_{V_{C1}} - \bar{K}_1 \alpha_1 \quad (25)$$

$$\dot{e}_{i_{L1}} = -K_2 e_{i_{L1}} - \bar{K}_2 \alpha_2 \quad (26)$$

where  $\alpha_1$  and  $\alpha_2$  represent the integral terms assuring zero error in steady state between the dynamics of  $V_{C1}$  and  $i_{L1}$ :

$$\dot{\alpha}_1 = K_1^\alpha e_{V_{C1}} \quad (27)$$

$$\dot{\alpha}_2 = K_2^\alpha e_{i_{L1}} \quad (28)$$

and  $K_1, \bar{K}_1, K_1^\alpha, K_2, \bar{K}_2$  and  $K_2^\alpha$  are positive tuning gain parameters that are used to impose desired dynamics to the

closed loop system.

From equations the first equation of (24),(25) and (27) We can then design:

$$i_{L1}^* = \frac{1}{R_1} (V_{PV} - V_{C1}) + C_1 K_1 (V_{C1} - V_{C1}^*) + C_1 \bar{K}_1^\alpha \alpha_1 \quad (29)$$

That feedback linearizes the first equation of 1. From equations the second equation of (24),(26) and (28) we can propose the control law:

$$u_1 = \frac{1}{V_{DC} + (R_{02} - R_{01})i_{L1}} [V_{DC} - V_{C1} + R_{02}i_{L1} - (L_1(K_2(i_{L1} - i_{L1}^*) + \bar{K}_2 \alpha_2) - C_1 \bar{K}_1 K_1^\alpha e_{V_{C1}} + (C_1 K_1 - \frac{1}{R_1})(K_1 e_{V_{C1}} + \bar{K}_1 \alpha_1))] \quad (30)$$

with the condition (always fulfilled in reality):

$$V_{DC} + (R_{02} - R_{01})i_{L1} \neq 0 \quad (31)$$

The control input  $u_1$  in its turn, feedback linearizes the dynamics of  $V_{C1}$  and  $i_{L1}$ . We can then introduce the Lyapunov function:

$$V_{1,2} = \frac{1}{2}([\alpha_1 e_{V_{C1}}]^T P_1[\alpha_1 e_{V_{C1}}]) + \frac{1}{2}([\alpha_2 e_{i_{L1}}]^T P_2[\alpha_2 e_{i_{L1}}]) \quad (32)$$

where matrices  $P_{1,2}$  satisfy the Lyapunov equation. The Lyapunov's function derivative is then:

$$\dot{V}_{1,2} = -e_{V_{C1}}^2 - \alpha_1^2 - e_{i_{L1}}^2 - \alpha_2^2 \quad (33)$$

and then we can conclude that the closed-loop dynamics of  $V_{C1}$  and  $i_{L1}$  are exponentially stable.

## V. STABILITY STUDY FOR THE INTERCONNECTED SYSTEM

It is possible to split the states into controlled variables  $V_{C1}, i_{L1}, i_{L2}, i_{L3}$  and  $V_{DC}$  and the zero dynamics (uncontrolled variables)  $V_{C2}$  and  $V_{C3}$ :

$$\begin{bmatrix} V_{C1} \\ i_{L1} \\ V_{C2} \\ i_{L2} \\ V_{C3} \\ i_{L3} \\ V_{DC} \end{bmatrix} = \begin{bmatrix} \text{Controlled dynamics} \\ \text{Controlled dynamics} \\ \text{Zero dynamics} \\ \text{Controlled dynamics} \\ \text{Zero dynamics} \\ \text{Controlled dynamics} \\ \text{Controlled dynamics} \end{bmatrix} \quad (34)$$

Let us now move the focus to the zero dynamics. We can rewrite the zero dynamics equations of system (1) as:

$$\begin{aligned} \frac{dV_{C2}}{dt} &= -\frac{1}{R_2 C_2} V_{C2} - \frac{1}{C_2} i_{L2} + \frac{1}{R_2 C_2} V_{BAT} \\ \frac{dV_{C3}}{dt} &= -\frac{1}{R_3 C_3} V_{C3} - \frac{1}{C_3} i_{L3} + \frac{1}{R_3 C_3} V_{SC} \end{aligned} \quad (35)$$

and we can compute  $V_{C2}^e$  and  $V_{C3}^e$  their equilibrium points:

$$\begin{aligned}
0 &= -\frac{1}{R_2 C_2} V_{C2} - \frac{1}{C_2} i_{L2}^* + \frac{1}{R_2 C_2} V_{BAT} \\
0 &= -\frac{1}{R_3 C_3} V_{C3} - \frac{1}{C_3} i_{L3}^* + \frac{1}{R_3 C_3} V_{SC}
\end{aligned} \quad (36)$$

and then define the complete equilibrium vector:

$$x^e = \begin{bmatrix} V_{C1}^* \\ i_{L1}^* \\ V_{C2}^e \\ i_{L2}^* \\ V_{C3}^e \\ i_{L3}^* \\ V_{DC}^* \end{bmatrix} \quad (37)$$

We define then the errors  $e_{V_{C2}}$  and  $e_{V_{C3}}$  as:

$$e_{V_{C2}} = (V_{C2} - V_{C2}^e)$$

$$e_{V_{C3}} = (V_{C3} - V_{C3}^e)$$

The Lyapunov candidate function  $V_{3,5}$  is then:

$$V_{3,5} = \frac{R_2 C_2}{2} e_{V_{C2}}^2 + \frac{R_3 C_3}{2} e_{V_{C3}}^2 \quad (38)$$

which time derivative is:

$$\dot{V}_{3,5} = -e_{V_{C2}}^2 - e_{V_{C3}}^2 \quad (39)$$

that again assures exponential stability of the zero dynamics.

We can now regroup these partial results in the form of the following theorem that establish the overall stability property.

**Theorem V.1.** : Consider system (1) and its equilibrium points set (37). Under the assumption that for each time the conditions:

$$V_{DC} + (R_{02} - R_{01})i_{L1} \neq 0$$

$$V_{DC} + (R_{04} - R_{03})i_{L2} \neq 0$$

$$V_{DC} + (R_{06} - R_{05})i_{L3} \neq 0$$

hold, then there exist control laws  $u_1, u_2$  and  $u_3$  given respectively by (30), (17) and (21); positive design parameters  $K_1, \bar{K}_1, K_1^\alpha, K_2, \bar{K}_2, K_2^\alpha, K_4, \bar{K}_4, K_4^\alpha, K_6, \bar{K}_6, K_6^\alpha, K_7, \bar{K}_7$  and  $K_7^\alpha$ , such that the closed loop system is exponentially stable.

*Proof.* We can finally state the constructive Lyapunov function for the whole system  $V_{1,2,3,4,5,6,7} = V_1 + V_2 + V_3 + V_4 + V_5 + V_6 + V_7$  as follows:

$$\begin{aligned}
V_{1\dots 7} &= \frac{1}{2}([\alpha_1 e_{V_{C1}}]^T P_1 [\alpha_1 e_{V_{DC}}]) + \frac{1}{2}([\alpha_2 e_{i_{L1}}]^T P_2 [\alpha_2 e_{i_{L1}}]) \\
&+ \frac{R_2 C_2}{2} e_{V_{C2}}^2 + \frac{1}{2}([\alpha_4 e_{i_{L2}}]^T P_4 [\alpha_4 e_{i_{L2}}]) + \frac{R_3 C_3}{2} e_{V_{C3}}^2 \\
&+ \frac{1}{2}([\alpha_6 e_{i_{L3}}]^T P_6 [\alpha_6 e_{i_{L3}}]) + \frac{1}{2}([\alpha_7 e_{V_{DC}}]^T P_7 [\alpha_7 e_{V_{DC}}])
\end{aligned} \quad (40)$$

It's derivative is then:

$$\begin{aligned}
\dot{V}_{1\dots 7} &= -e_{V_{C1}}^2 - \alpha_1^2 - e_{i_{L1}}^2 - \alpha_2^2 - e_{V_{C2}}^2 - e_{i_{L2}}^2 - \alpha_4^2 - e_{V_{C3}}^2 \\
&- e_{i_{L3}}^2 - \alpha_6^2 - e_{V_{DC}}^2 - \alpha_7^2 - e_{V_{C2}} e_{i_{L2}} - e_{V_{C3}} e_{i_{L3}}
\end{aligned} \quad (41)$$

that completing the squares can be rewritten as:

$$\begin{aligned}
\dot{V}_{1\dots 7} &\leq -e_{V_{C1}}^2 - \alpha_1^2 - e_{i_{L1}}^2 - \alpha_2^2 - \alpha_4^2 - \alpha_6^2 - e_{V_{DC}}^2 - \alpha_7^2 \\
&+ \left(1 - \frac{1}{\sqrt{2}}\right) (-e_{V_{C2}}^2 - e_{i_{L2}}^2 - e_{V_{C3}}^2 - e_{i_{L3}}^2)
\end{aligned} \quad (42)$$

This in its turn assures again the exponential convergence of all states of the interconnected system towards their equilibrium points.  $\square$

## VI. SIMULATION RESULT

MATLAB/Simulink is used for simulation studies of a DC microgrid consisting of solar, battery and supercapacitor units supplying a variable resistive load. The system configuration is as shown in Figure (1) and the parameters were discussed in Section two. The model parameters' values are depicted in table (I) and (??).

$R_{02}, R_{04}, R_{06}$	0.045Ω
$R_{01}, R_{03}, R_{05}$	0.044Ω
$R_1, R_2, R_3$	0.14Ω
$C_1, C_2, C_3$	4700e <sup>-6</sup> F
$C_{DC}$	1500e <sup>-6</sup> F
$L_1, L_2, L_3$	100e <sup>-6</sup> H
$V_{DC}$	50V

Table I: DC Microgrid paramaters

The main goal is to demonstrate that the proposed strategy provides good results for cases of varying solar irradiance as well as load variations. It is expected to regulate the DC bus voltage with battery and supercapacitor units in such a way that the supercapacitor provides the fast changing components of the required DC bus voltage compensating current while the battery unit provides the slow varying components. This is achieved with a signal decomposition block with a 20Hz LPF.

As in the section of the experimental results, since the voltages in the SC and battery did not vary much, their waveforms are not shown.

Figure (4) shows how the DC bus voltage (top) varies as the load current and PV current (screens just below) vary. The load impedance is equal to 14.7Ω for 0.06s < t < 0.35s and t > 0.5s and is equal to 11Ω elsewhere. The solar irradiance was rated for t < 0.26s and t > 0.46s, and equal to 800W/m<sup>2</sup> otherwise. There, one sees that the DC bus voltage varies briefly following the transients, quickly recovering and stabilizing at the rated (50V) DC bus voltage in about 1ms. The maximum DC bus voltage error is equal to 0.04V.

This is achieved by the action of the proposed control scheme making use of the battery and supercapacitor interfaces. The reference and actual waveforms of the former are shown in the fourth screen from the top. The reference signal varies

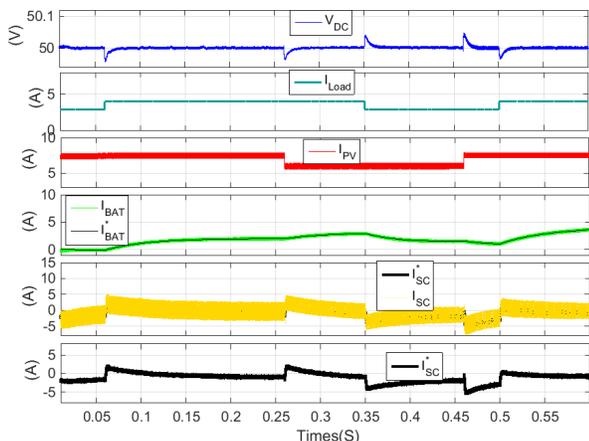


Figure 4: Dynamic response of the system

slowly and the actual waveform follows it with a small switching frequency ripple. Besides, the battery current is the one required to balance the power in the DC microgrid in steady-state. The reference and actual waveforms of the supercapacitor unit are shown in the fifth screen from the top, with the second following closely the first. This reference is again presented in the bottom screen for better visualization, and varies fast, following PV generation and load demand variations, returning to 0A after transitory.

These results confirm that the proposed scheme is effective in achieving the goals defined for this application.

## VII. EXPERIMENTAL INVESTIGATION AND DEMONSTRATION

A DC Microgrid with a RES, two energy storage units and a variable load was assembled in the laboratory to verify the performance of the proposed control scheme experimentally. An Agilent Solar Array Simulator (*SAS*) (*E4350B*) was used to emulate a solar array. It was programmed to provide a maximum power of 213W with 7.35A and 29V at rated solar irradiance.

Maxwell supercapacitor modules (*BMOD0165P048*) of 165F with a rated voltage of 48V were employed for the energy storage units. One is used as a battery and the second is used as a supercapacitor.

Regarding the power electronics interface, a Semikron *MiniSKiiP8* Three-phase 1200V Power board, with a *MiniSKiiP 83AC* power module and a *SKHI61* IGBT driver, was used in this study. One leg of the three-phase inverter is used for each DER. An LC filter (100μH and 470μF) is connected between the mid-points of the three-phase inverter legs and the storage and the source elements, to create the classical bi-directional Class C DC-DC converters. A 1500μF capacitor at the DC bus of the inverter corresponds to the main filter at the DC bus of the Microgrid.

The converters switch at 20kHz. A 20μs time step is used in this case. The reference voltage for the DC bus is kept at 50V. A set of ten parallel switchable 44Ω resistors were used for realizing a variable load, the battery is in discharge mode.

A picture of the experimental set-up is shown in the following Figure.

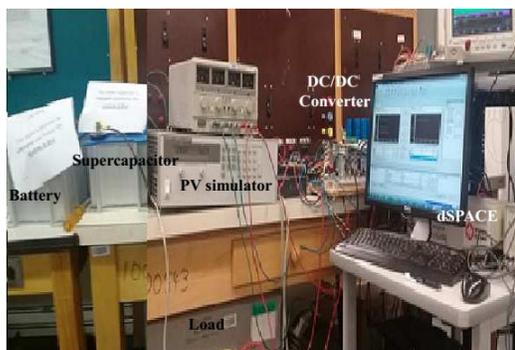


Figure 5: Experimental setup

The main objective of the tests is to observe the performance of the system, mostly the regulation of the DC bus voltage and the split of the required compensating current among the supercapacitor and battery converter units. In this study, the cut-off frequency of the LPF is equal to 20Hz, as in the simulations. The voltages in the supercapacitor and battery are equal to 24 V and 28 V, respectively. Considering the short time frames addressed in the performance results shown in this study, there should be no noticeable change in their voltage magnitudes, which are not shown in the results presented below.

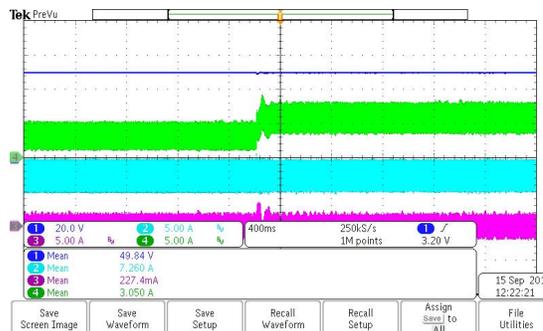


Figure 6: screen shot of the scope

Figure (6) shows a screen shot of the scope with the response of the system (DC bus voltage in blue, battery current in green, PV current in light blue and supercapacitor current in pink) to an increase in the power demand, when the load is changed from 8.8Ω (five resistors of 44Ω in parallel) to 7.35Ω (six resistors of 44Ω in parallel). Since it is difficult to see the details of the waveforms, the stored data was sent to MATLAB where it is easier to rescale the waveforms emphasizing their key features for performance analysis.

This is what is shown in Figure (7). In such a case, the load changed at  $t = 1s$ . On the top, one sees that the DC bus is initially equal to 50V, decreasing briefly by about 0.9 V following the load step, then returning, in about 0.15s, towards the original value. In the next plot, the current in the inductor of the PV interface remains virtually unaffected at the MPPT value. Just below, one sees the inductor currents of the battery and supercapacitor interfaces. The former initially provides

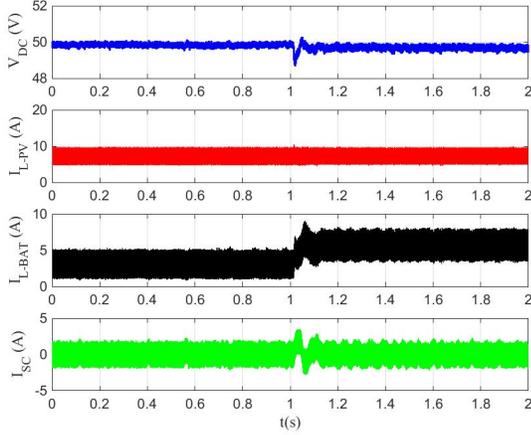


Figure 7: Dynamic Response during increasing load with battery discharging

about  $3.1A$ , increasing its output rather slowly, to meet the average value of the step load increase at  $t = 1s$ . Conversely, in the very bottom, the inductor current of the supercapacitor interface starts at  $0A$ , showing that it is not providing any average power. Following the load increase, the contribution of the supercapacitor converter increases very fast and returns to an average of about  $0A$ , as the battery interface takes over the power balancing in the system.

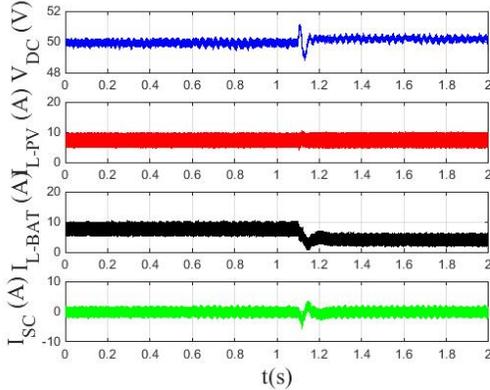


Figure 8: Dynamic Response during decreasing load with battery discharging

Figure (8) shows the opposite change: A step load decrease at  $t = 0.11s$ . In such a case, the DC bus voltage will initially increase, by about  $0.9V$ , then return to its original value as the battery and supercapacitor interfaces adjust their injected powers. As before, the supercapacitor interface reacts faster than the battery one, what allows the current in the latter to vary at a lower rate, as desired and expected.

Figure (9) shows a step load decrease at  $t = 0.6s$ . The DC bus voltage increase, by about  $0.9V$ , then return to its original value. The supercapacitor absorbs exceeding power for a very short time while battery adjusts surplus of power.

Figure (10) shows a step load increase at  $t = 0.7s$ . The DC bus voltage decrease by about  $1.1V$ , then return to its original

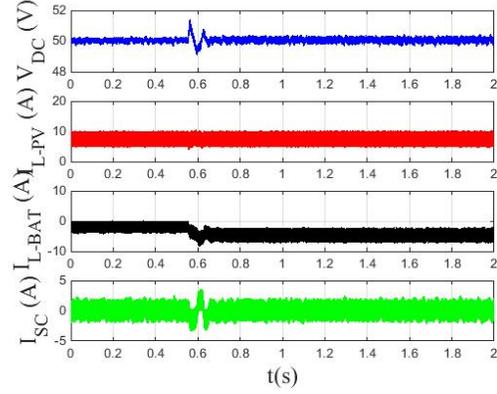


Figure 9: Dynamic Response during decreasing load with battery charging

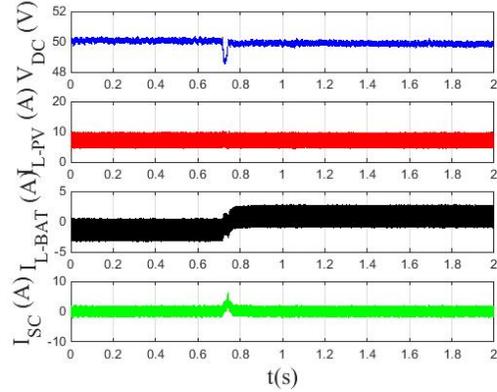


Figure 10: Dynamic Response during increasing load with battery charging

value as the battery and supercapacitor interfaces adjust their injected powers.

As before, the supercapacitor interface reacts faster than the battery one, what allows the current in the latter to vary at a lower rate, as desired and expected.

## A. Results

The response for step power changes is fast, returning to nominal value of  $V_{DC}$  in about  $t = 0.04s$ . The  $V_{DC}$  output has small ripple, showing that the power quality is good, and that we can reduce the size of capacitor in the converter.

## VIII. CONCLUSION

DC MicroGrids offer an ideal environment for the integration of large amounts of stochastic renewable energy sources. Sudden and large power variations, generation and demand, can be prevented by means of a hybrid (power and energy) energy storage system. This is usually accomplished with linear type controllers designed based on linearized plant models, for a certain operating condition. As this changes, so do the dynamic responses of key system variables. In such cases, the system stability cannot be assure in the strict sense. As an alternative, this paper has discussed the use of a two

level hierarchical control strategy that ensures the stability of all variables, in particular the voltage of the DC bus, in spite of strong and random variations of power generation and demand. The low level controller is based on nonlinear theory and assures exponential stability of each element as well as of the whole system under a wide range of operating conditions. On the top of that, a high level controller provides references to accomplish the appropriate power flow among system components. A rigorous stability analysis, that takes into account the whole system by a constructive Lyapunov function, is performed. Unlike conventional linear approaches, the proposed control structure will assign desired dynamics to variables of the DC MicroGrid, and along with a final Lyapunov function, will provide the required controller parameters. It should be noted that the obtained control algorithms are very simple, close to nested PIs with feedforward compensating terms. They can be easily implemented in real applications, without the need of sophisticated digital control hardware. Simulation and experimental results of a DC MicroGrid with a Photovoltaic source, two storage units and a variable load are presented to corroborate the theoretical analysis, proposed control scheme and design procedure. It is shown that the system presents a good dynamic performance under various operating conditions created by strong random variations of power generation and consumption. Future works will address the comparison of the proposed controllers with classical ones, in terms of performance and required resources.

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